

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Eliyahou Harari et al.

Title:

Multi-State Non-Volatile Integrated Circuit Memory Systems That Employ

Dielectric Storage Elements

Application No.:

10/002,696

Filing Date:

October 31, 2001

Examiner:

Weiss, Howard

Group Art Unit:

2814

Docket No.:

SNDK.272US0

Conf. No.:

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on <u>December 18, 2003</u>

May 8 Brezzi

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT

Sir:

This is in response to the non-final Office Action dated August 7, 2003.

Claim Amendments are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks begin on page 10 of this paper.

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